



INVESTOR PRESENTATION · 2026

Trusted silicon for infrastructure that cannot fail offline.

Built for unstable power, heat, and field conditions across the Global South.

SecureGrid (S-Series) · InferEdge (EA Series) · TrustCore™
Pre-seed · ~\$4.5M · SAFE · \$14M cap

Dr. Michael Apasiku | Founder & CEO

nelix.io

Investment Highlights

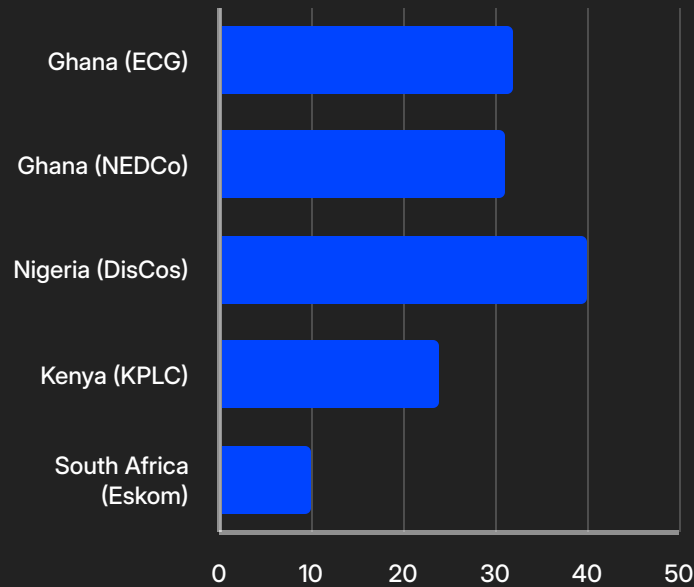
Metric	Label	Details
~\$4.5M	PRE-SEED ASK	SAFE · \$14M cap
2029	MPW SHUTTLE	Seed-funded · Q2
\$20.7M	Y5 REVENUE (2031)	Base case model
55nm	MATURE NODE	Capital-efficient
Ghana	BEACHHEAD	ECG engagement

De-risking: Pre-seed funds FPGA validation & LOIs — Seed \$10M (2028) funds MPW & first paid pilots, not this round.

Challenges in African Grid Efficiency

Revenue loss, distribution inefficiencies, and key drivers

Distribution Loss Rates by Market



Ghana ECG Annual Revenue Gap

> \$200M

due to tampering & offline meters

Key Drivers: Insecure hardware (generic MCUs), performance degradation in harsh environments, visibility gaps during outages, and persistent regulatory scale challenges.

Regulated loss rates range from 24% to 40% across anchor markets — a massive opportunity for secure, tamper-detecting metering silicon built for the Global South.

Why Now?

Policy, sovereignty, and mature-node economics converge

Market Convergence: Driving Immediate Opportunity



01 Sovereignty

Supply-chain risk driving demand for localized, auditable silicon in critical infrastructure.



02 Policy Tailwinds

Leveraging Mission 300, AfCFTA, and AfDB/DFI grid modernization initiatives for continental deployment scale.



03 Technology Window

RISC-V + 55nm mature node enables capital-efficient fabless entry, avoiding high-capex 7nm AI competition.

NELIX BEACHHEAD STRATEGY: GHANA UTILITY ENGAGEMENT → MEA OEM SCALE → PLATFORM LICENSING

The alignment of geopolitical mandates and capital-efficient technological shifts creates a unique window for market entry.

NELIX · Platform Strategy

One trust layer. Two silicon products. One deployment platform.



SecureGrid

(S-Series)

Secure metering and industrial MCU designed for utilities and OEM meters.



InferEdge

(EA-Series)

Offline edge AI accelerator for grid edge, telecom, and AgriTech applications.



TrustCore™

The foundation of trust providing PUF, PQ-ready crypto, and secure boot, licensed on every die.



Compute Platform

Enables attestation, secure OTA, and SDK access for recurring revenue and platform lock-in.

Pre-silicon Validation Path

Strategic development milestones and risk mitigation roadmap



Risk mitigation strategy focuses on staged capital deployment (Seed \$10M for 2028 MPW) and scaling the core team from 8 to 16 FTEs.

Traction & Validation

Progressing toward pre-seed milestones



ECG (Ghana)

Technical engagement: Active spec review; no commercial contract yet.



African Development Bank

Proposal submitted: Continental AI initiative currently at the proposal stage.



AfCFTA Secretariat

Presentation delivered: Ongoing policy and ecosystem engagement.



FPGA Design Partners

In discussion: Targeting paid architecture validation.



Applied Intelligence

Commercial: Providing AI training and pilot services via nelix.io/services.

Pre-seed targets: FPGA platform live · 2 design-partner LOIs · architecture freeze by Q4 2027 — no commercial revenue in Y1

Bottom-up Market Strategy

MEA Smart Metering Wedge · Y5 Base Case (2031)

**\$42B MEA
Smart Metering
TAM**

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TAM**

Total addressable market
(2031) for context only.

**\$12–20M Near-
term Program
SAM**

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Program SAM**

3 utilities × 500K–800K × \$8

**\$10M+
SecureGrid
Chip Revenue**

**\$10M+ SecureGrid Chip
Revenue**

Y5 base case projection of
1.25M units.

**\$20.7M Total
Y5 Revenue**

\$20.7M Total Y5 Revenue

Aggregate of chips, IP,
platform, and services.

Strategic pillars include Sub-Saharan Africa expansion, InferEdge attachment, Mission 300 alignment, and diversification into telecom and AgriTech. First commercial revenue slated for 2028.

Unit economics at scale

Model-aligned ASP and gross margin build-up

Volume tranche	SecureGrid ASP	InferEdge ASP	Blended GM
Pilot · 10K units	\$12	\$28	~45%
Scale · 100K units	\$10	\$22	~60%
Production · 1M+ units	\$8	\$20	~70–76%

Y5 base case: \$20.7M revenue · ~76% blended gross margin · ~\$900K EBITDA

Revenue Model

Three streams · modular · capital-efficient ramp



Chip sales

SecureGrid at \$8–10 and InferEdge at \$20 ASP at scale; volume-driven model targeting utility & OEM sectors.



IP licensing

TrustCore security blocks offering high-margin, recurring revenue streams.



Platform & services

Includes SDK, firmware, and Applied Intelligence; serves as bridge revenue for years 1–2 using non-dilutive funds.

Scalable framework designed for capital efficiency and long-term recurring growth.

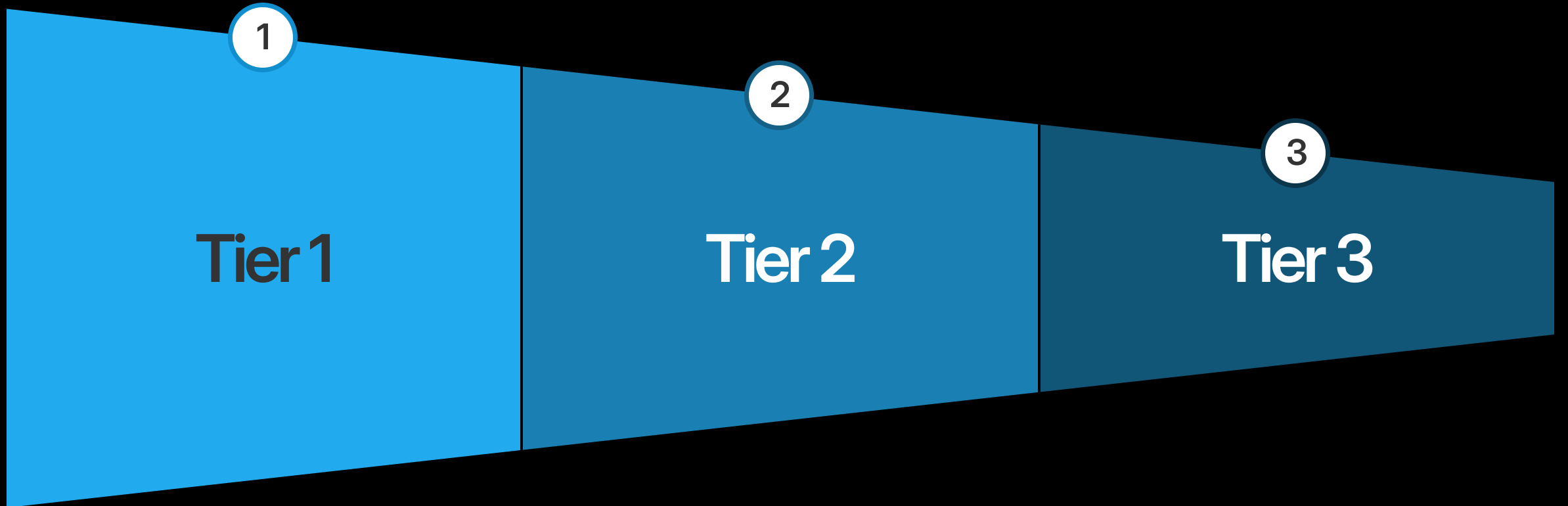
Competitive Positioning

Named tiers — not generic checkmarks

	Generic MCU	Mindgrove	Nelix
HW root of trust (PUF)	×	Partial	✓ Native
Offline edge AI on-die	×	Roadmap	✓ InferEdge
African grid / field conditions	×	✓ India	✓ Built for
Metering + AI security integrated	×	×	✓
Infrastructure Compute platform	×	×	✓

Go-to-market Strategy

Ghana anchor → MEA OEMs → platform licensing



Anchor

ECG (Ghana) utility pilot with active technical engagement.

OEM Scale

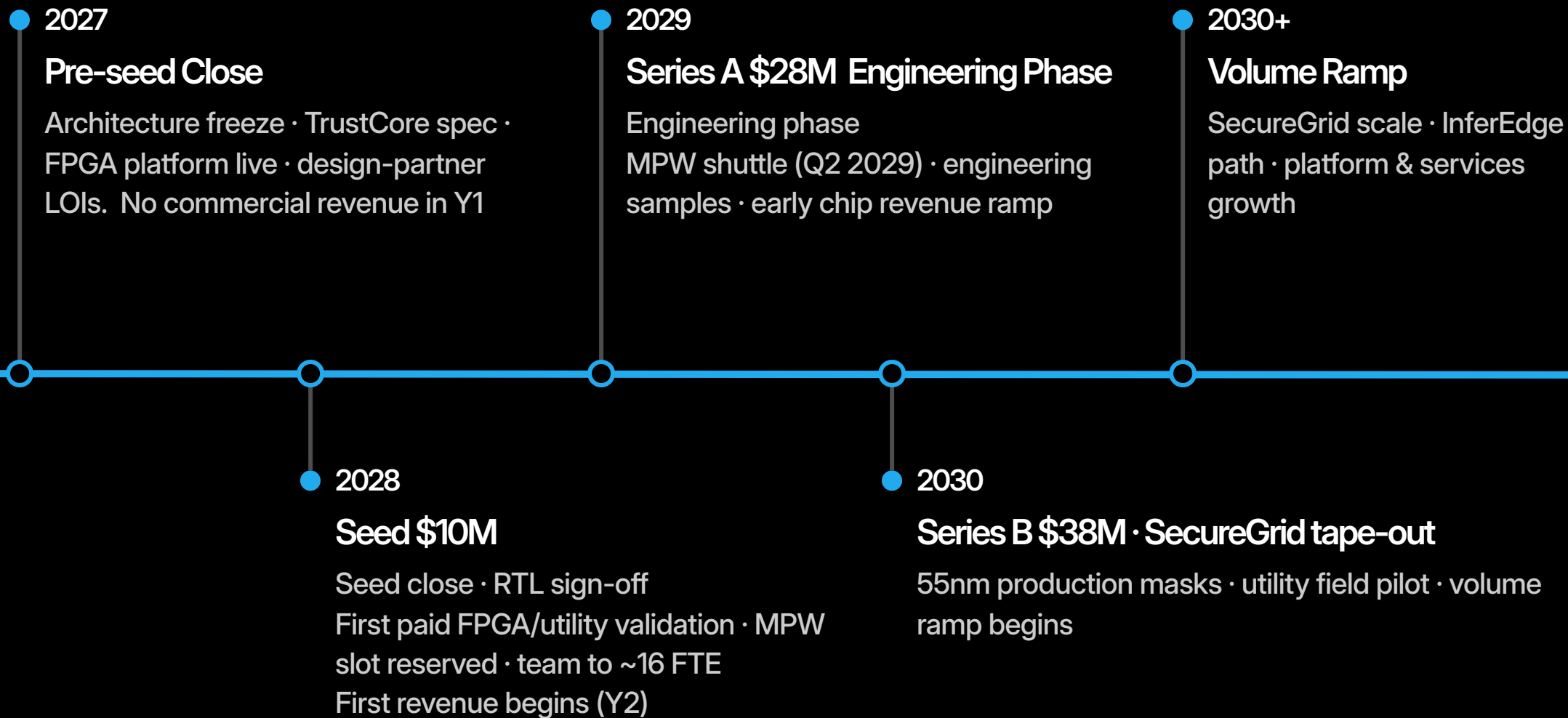
Itron, Landis+Gyr, and local meter OEMs identified for partnership.

Edge Expansion

Scaling via Telecom, AgriTech, and national platforms through ecosystem discussions.

Roadmap & Financing

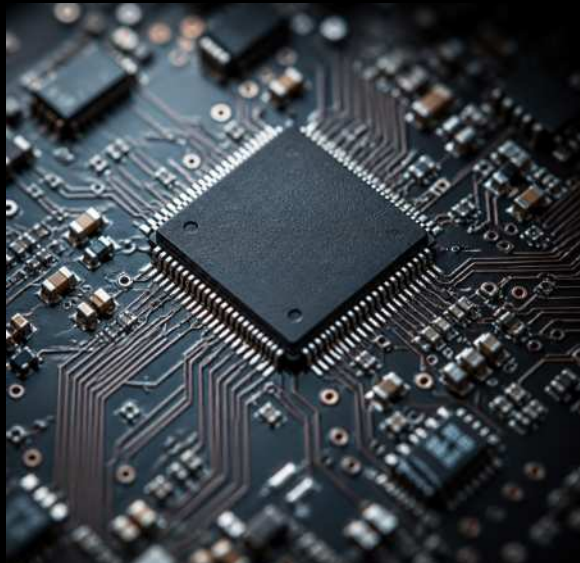
Product milestones aligned to funding gates



Comparable Semiconductor Pre-Seed Raises

Nelix valuation and raise is in-range for infrastructure silicon

Mindgrove



Raised \$2.3M at Seed stage for RTL + secure IoT SoC. Shares same metering wedge and geographic focus as Nelix.

Chipmind



Raised \$2.5M at Pre-seed for AI EDA + chip team build. Similar capital ask for pre-silicon development.

GEMESYS



Raised €8.6M at Pre-seed for working HW demonstrator. Represents a higher technical proof bar with EU grant support.

Nelix (This Round)



Targeting ~\$3.6M at Pre-seed for FPGA + utility engagement. Positioned as an infrastructure anchor in the African market.

Decades of Industry Driven Experience

A powerhouse collective of industry veterans from Intel, AMD, and Texas Instruments



Dr Michael Apasiku

Founder & CEO
Advanced Silicon
Packaging



Dr Felix Njeh

Co-Founder President
System Validation



**Dr Abiodun
Anifowose**

Head of Software
Engineering
AI Software Architect



Isaac Danso

FPGA & Chip Design
Engineer



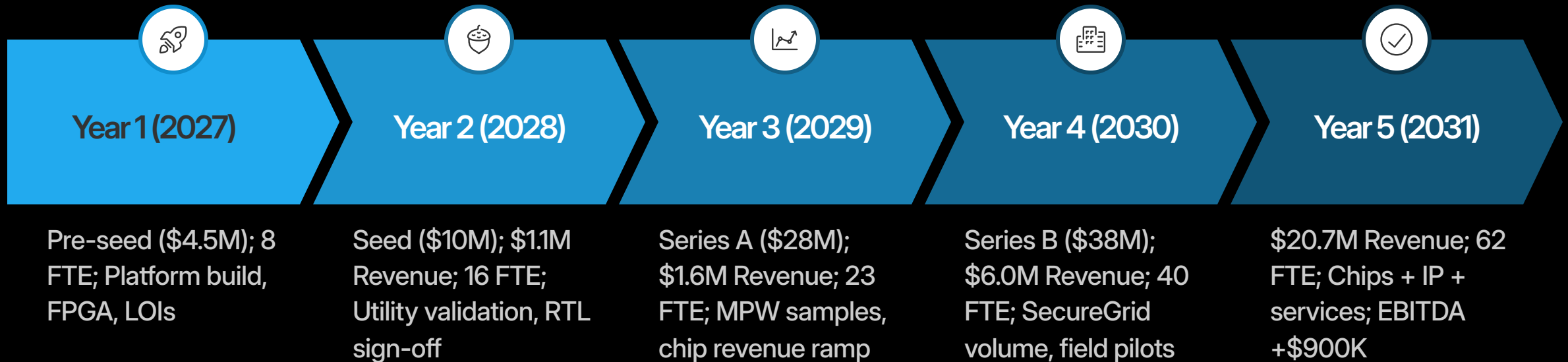
Dr Emmanuel Nti

Analogy & Mixed Signal
Design Engineer

Over 20 years of cumulative experience delivering resilient silicon solutions for the most demanding global environments.

Financial Summary: 5-Year Projection

Base case trajectory from Pre-seed to Series B scale



Strategic funding path: Pre-seed → Seed → Series A → Series B. Cash-flow positive by Y5 (2031).

Pre-Seed Funding Overview

Raising ~\$3.6M to reach critical architectural milestones

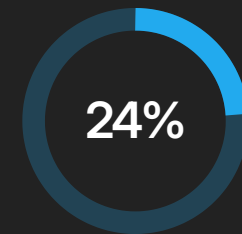
Funding Target

\$4.5M

Valuation Cap

\$14M

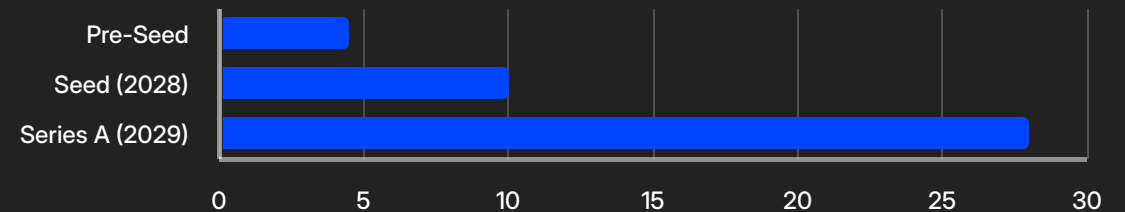
Dilution



Funding Milestones (M1-M5)

M1 — Architecture freeze & TrustCore™ spec M2 — FPGA validation platform live M3 — Design-partner LOIs & pilot program scoped M4 — RTL sign-off gate & verification environment M5 — First paid FPGA/utility validation + MPW slot

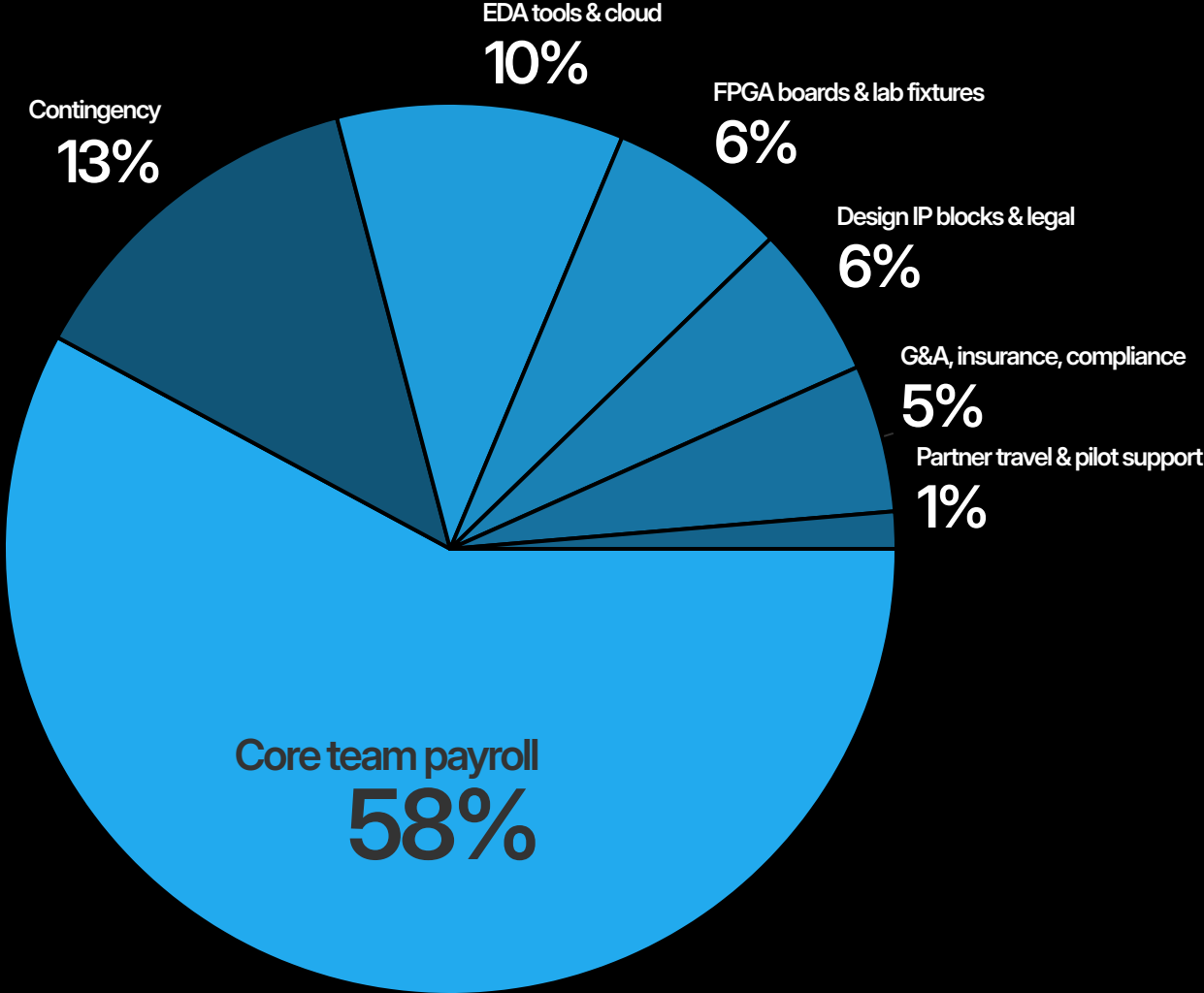
Capital Roadmap



Note: Pre-seed does NOT buy tape-out or guarantee Y1 revenue.

Pre-Seed Use of Funds

Breakdown of projected capital allocation






Contact & Investment


Accra, Ghana | USA

Investment: \$3.6M pre-seed (SAFE, \$14M cap)

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Data Room Assets available: Financial Model v2, TrustCore threat model, and FPGA evaluation one-pager.